

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 63-127303
 (43)Date of publication of application : 31.05.1988

(51)Int.Cl.

G05B 19/02

G06F 15/16

(21)Application number : 61-271921

(71)Applicant : TOSHIBA CORP

(22)Date of filing : 17.11.1986

(72)Inventor : ASHIDA KAZUHIDE

(54) PROGRAMMABLE CONTROLLER

(57)Abstract:

PURPOSE: To improve the executing efficiency as a whole by counting a sequence unit number shared by each CPU unit and distributing the load so that the count number of each CPU unit is averaged.

CONSTITUTION: A common operation system and the same program are stored in a program memory of CPU units 1A, 1B and the sequence unit included in the program is shared by the units 1A, 1B and executed in parallel. In the process of execution, total execution count numbers N1, N2 are obtained for the units 1A, 1B periodically respectively and the quantity is compared, and in case of $N1 \geq N2$, the sequence unit having a minimum (m1) execution count of the unit 1A is transferred to the unit 1B. Thus, since the relation of $N1 = N1 - m1$, $N2 = N2 + m2$ is obtained, new N1, N2 are compared further based thereupon to make the total execution counts nearly the same to the utmost. As a result, the high speed processing by plural CPU units and the averaged load are attained.

